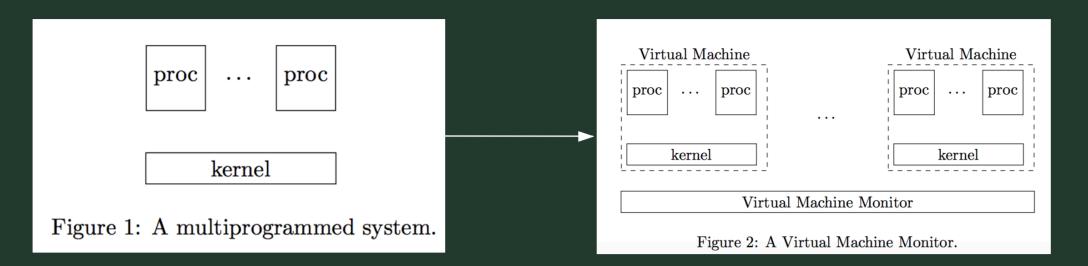
In the hardware-assisted virtualization technique we try to execute the instructions of the target machine directly on the host processor, as much as possible.

The Virtual Machine Monitor



Emulating the target CPU When to trap? any instruction that affects the VMM or the other VMs cannot be directly executed

Emulating the target Physical Memory

Emulating the target I/O devices Emulating interrupts

The Intel VMX technology



The state of the processor is loaded from here during a VM enter and stored back here during a VM exit Whenever the system code tries to execute an instruction that would either violate the isolation of the VMM, Guest state .____ or that must be emulated via software, the hardware can trap it and switch back to the VMM. **VMLAUNCH** instructions Host state ----- The state of the processor is loaded from here during a VM exit enters non-root mode **VMRESUME** instructions → VM execution control ------ specify what is allowed and what is not allowed during non-root mode; The VMCS data structure has several fields unallowed actions will cause a VM exit returns to root mode ------ VM exits VM enter control ----- contains several flags and fields that determine some optional behaviors of the root to non-root transition VM exit control ----- contains several flags and fields that determine some optional behaviors of the non-root to root transition

VM exit reason ----- contain several informations related to the reason

that caused the latest VM exit